7 16 S ∞ 139 Diaphragm Control Circuit Motor Control Circuit Built-in/ Flash Circuit Shutter/ Control Circuit AF Circuit 9 13b 13a Pt Pu Ph 运 ď RAM ROM SPU P13 Ъ Ъ P ď Ó S φ Communication Display Panel Communication Information Camera-lens Camera-flash **EEPROM** Interface Interface \mathfrak{C} pp/ DC/DCon 0 K Q X Q Regulator 2a Fig.

22

TTL Photometering Circuit

꿈

A 23

Photometering Circuit

Pm

۰ ۲ ۲

ō

Setting Switches O

SWS SWR SWM

P12 P11 P10

Information

တ

TTL Direct

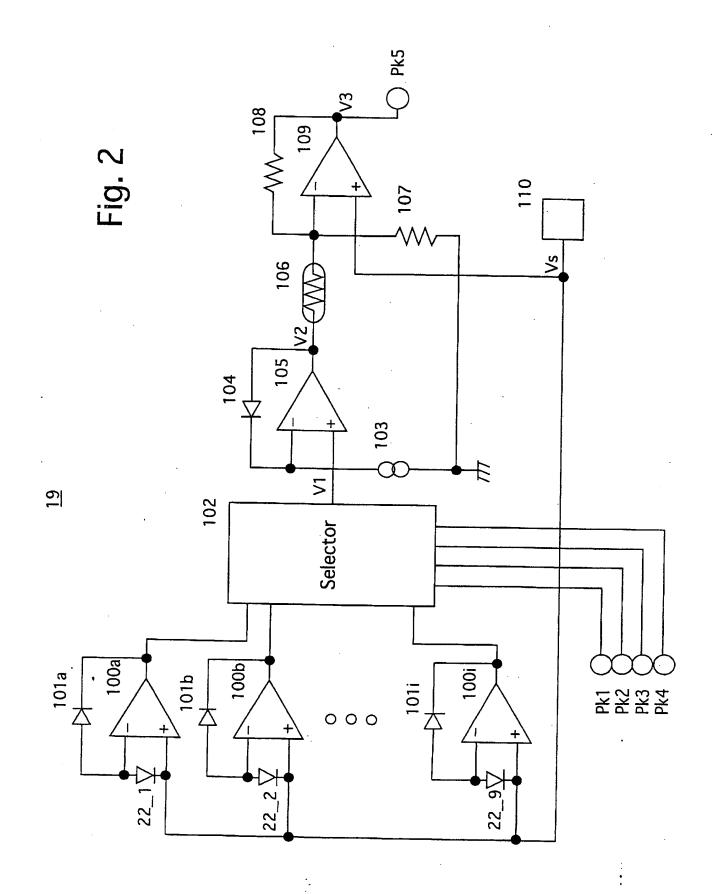


Fig. 3

<u>50</u>

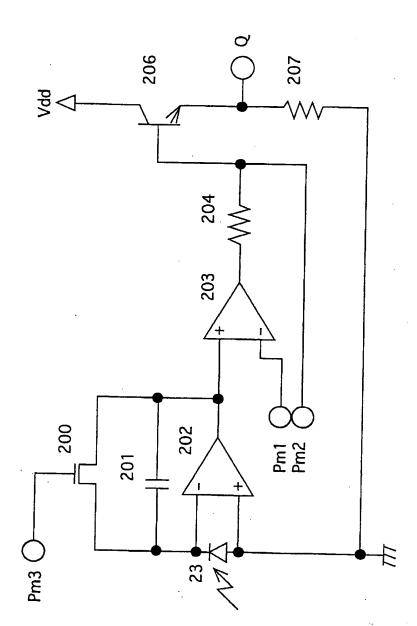


Fig. 4A

20

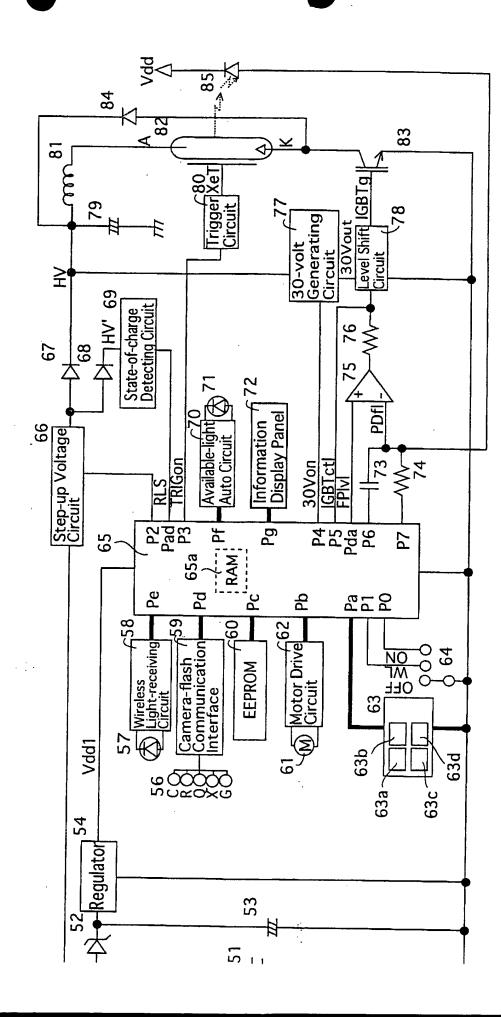


Fig. 4B

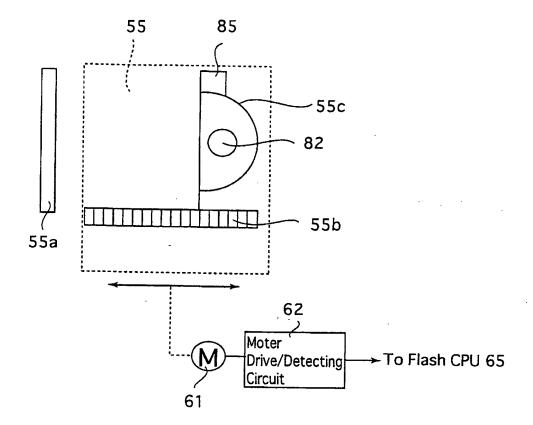


Fig. 5

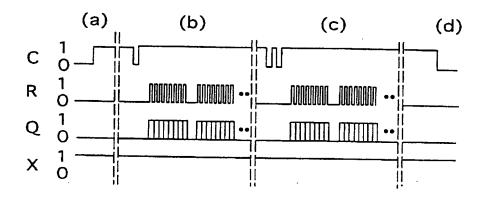
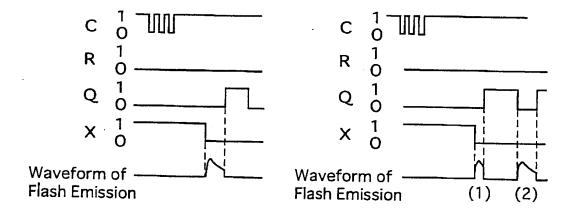
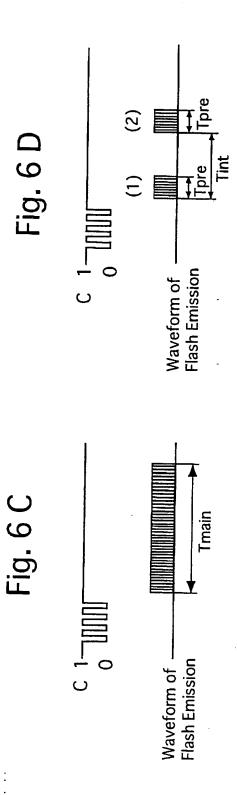


Fig. 6 A

Fig. 6 B





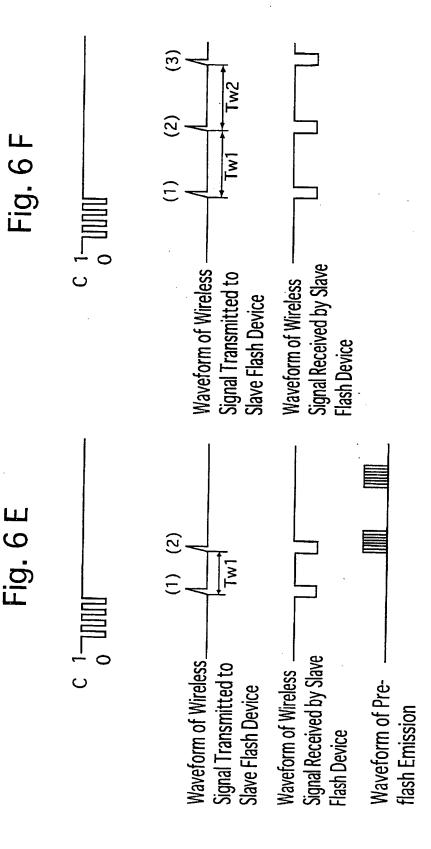


Fig. 7

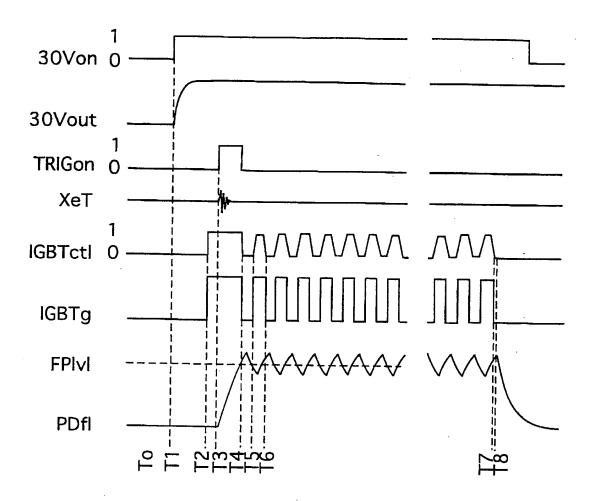


Fig.8A

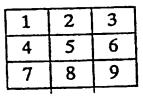


Fig.8C

4	12	4
12	36	12
4	12	4

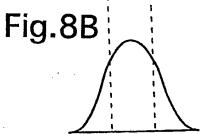


Fig.9A



Fig.9B

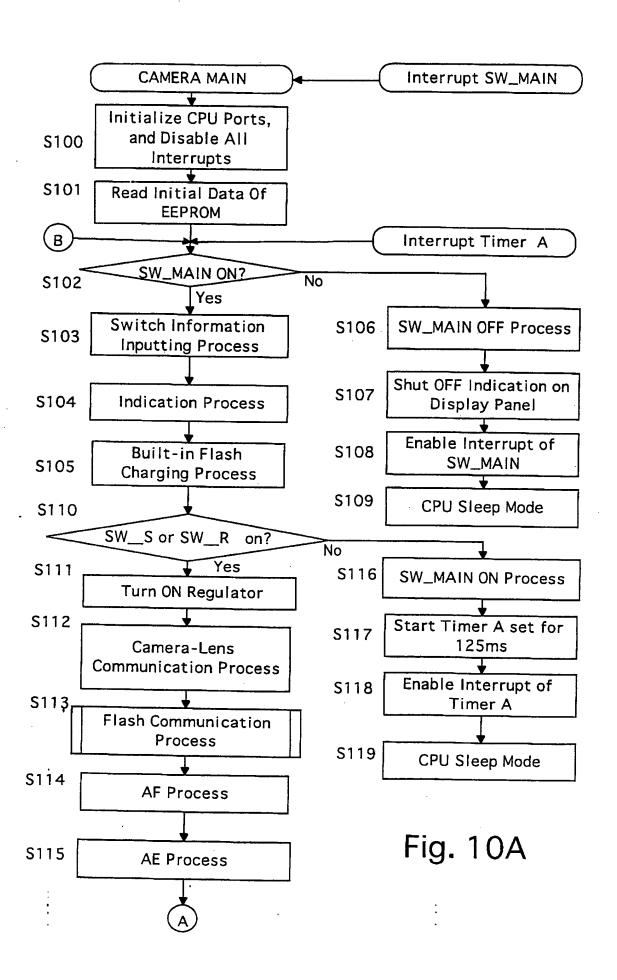
1	1	1
1	4	1
1	3	1

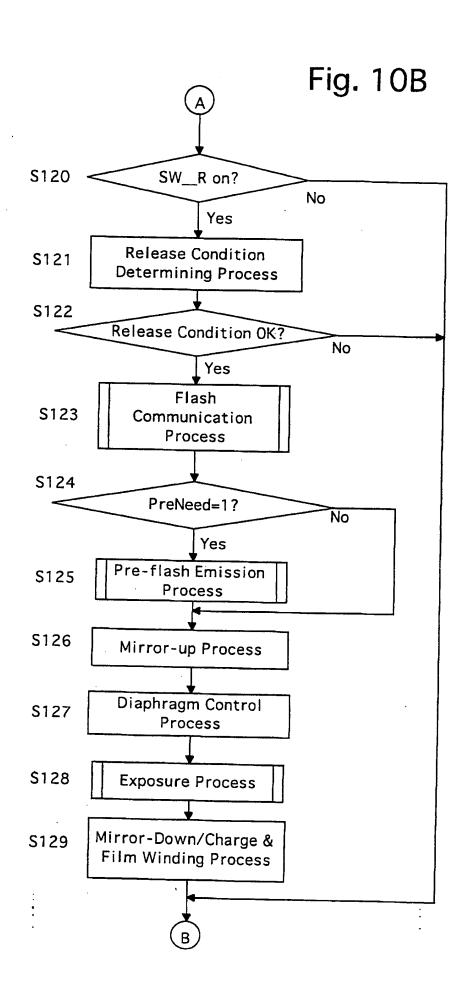
Fig.9C

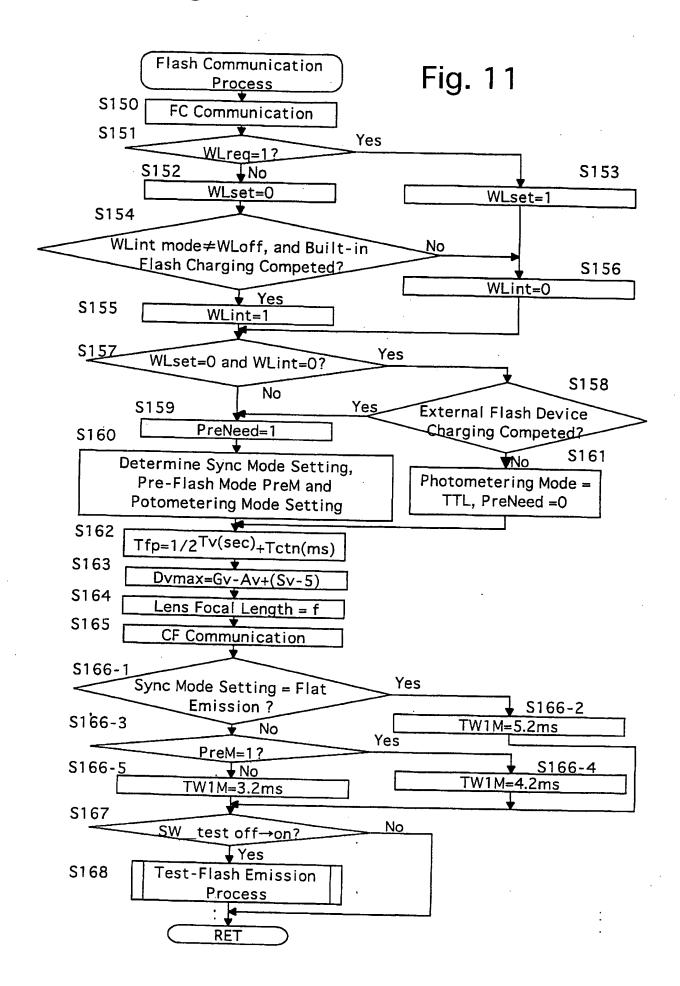


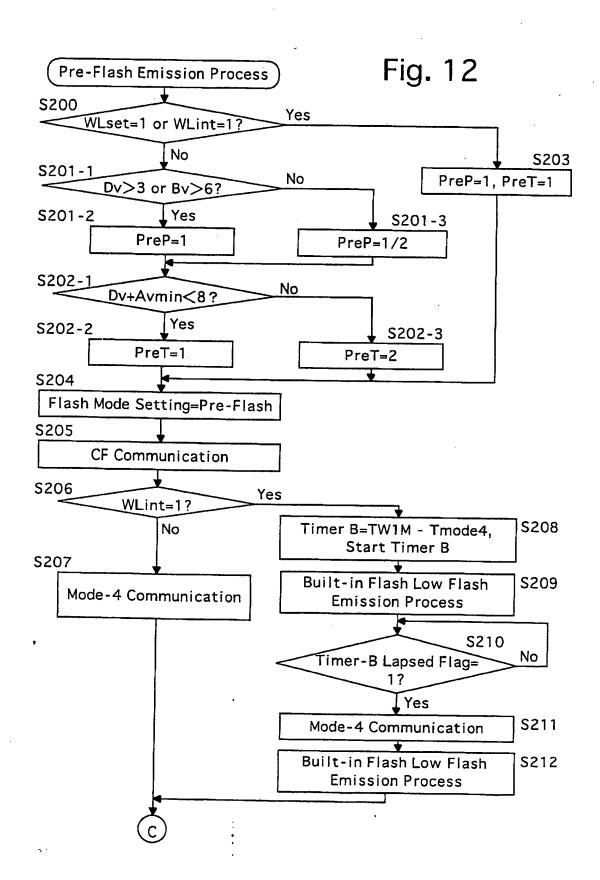
Fig.9D

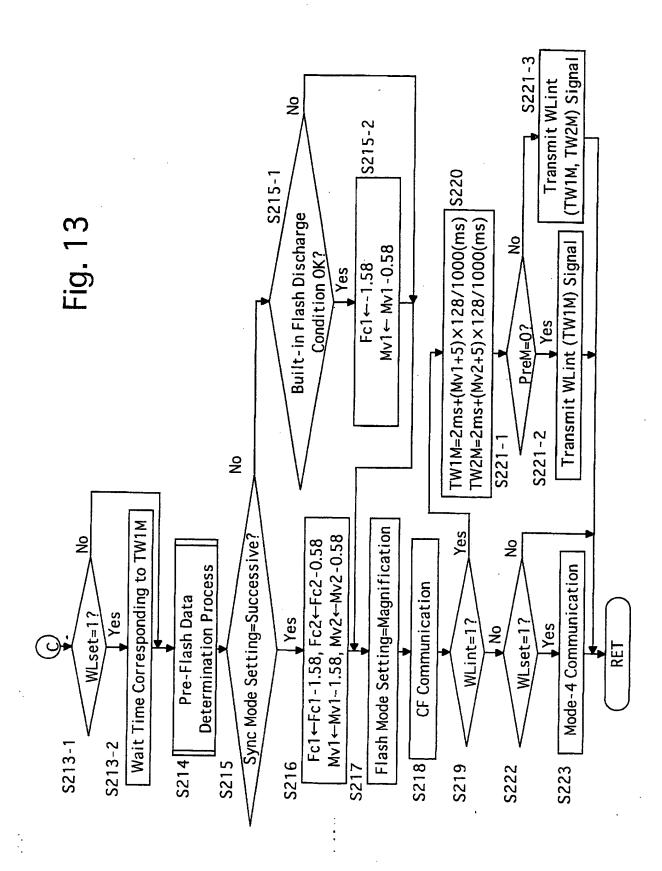
4	4	7
4	4	7
4	4	7

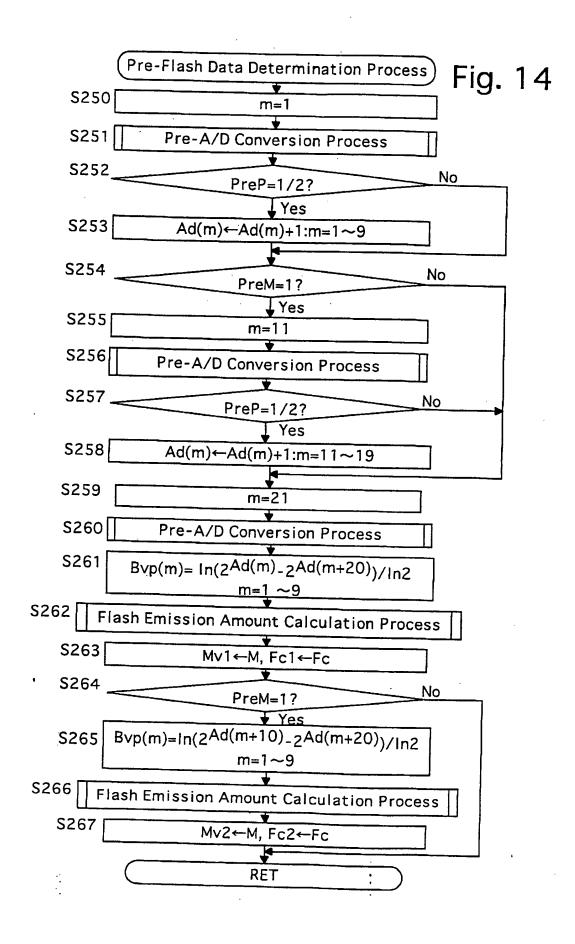


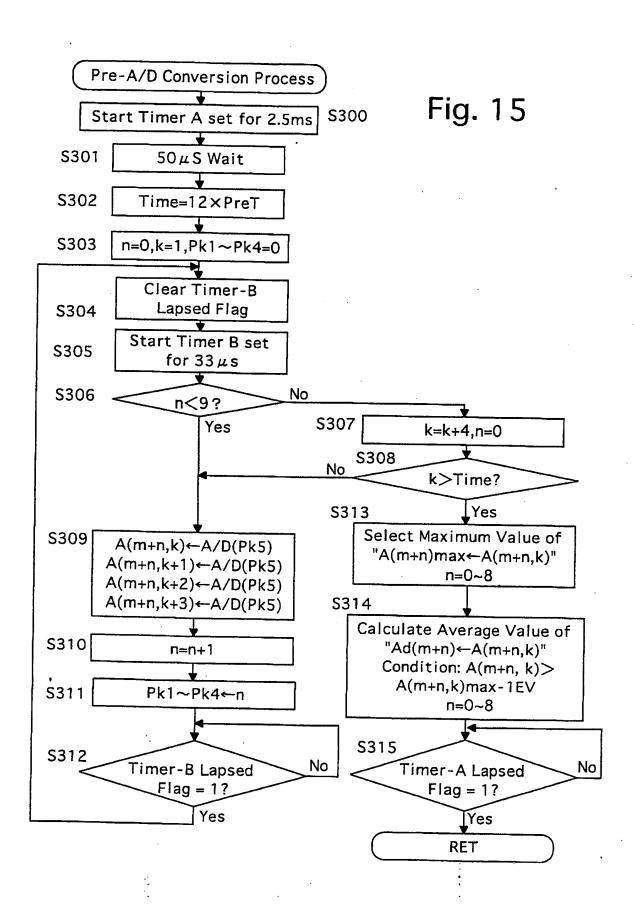


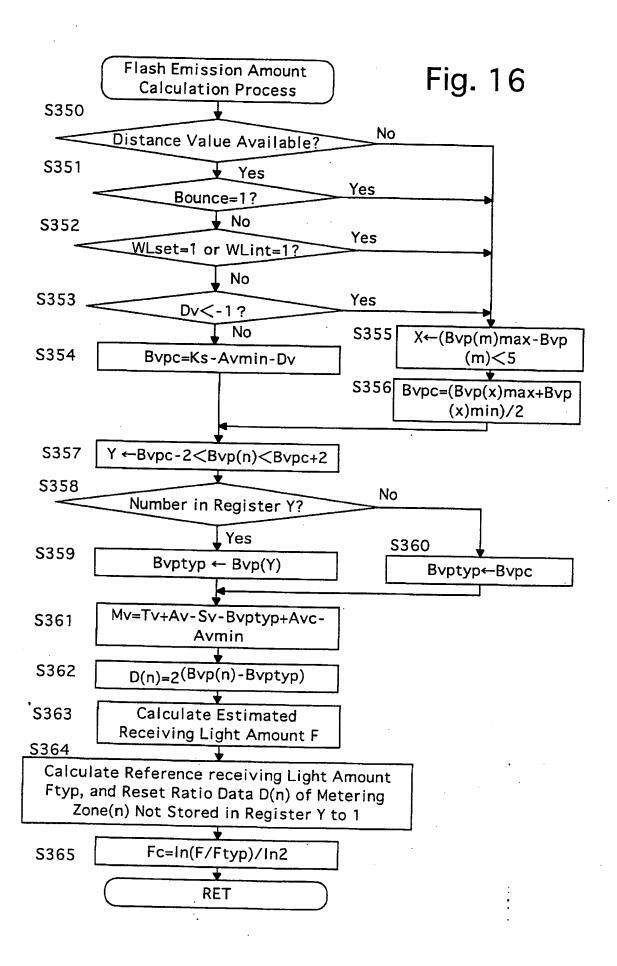


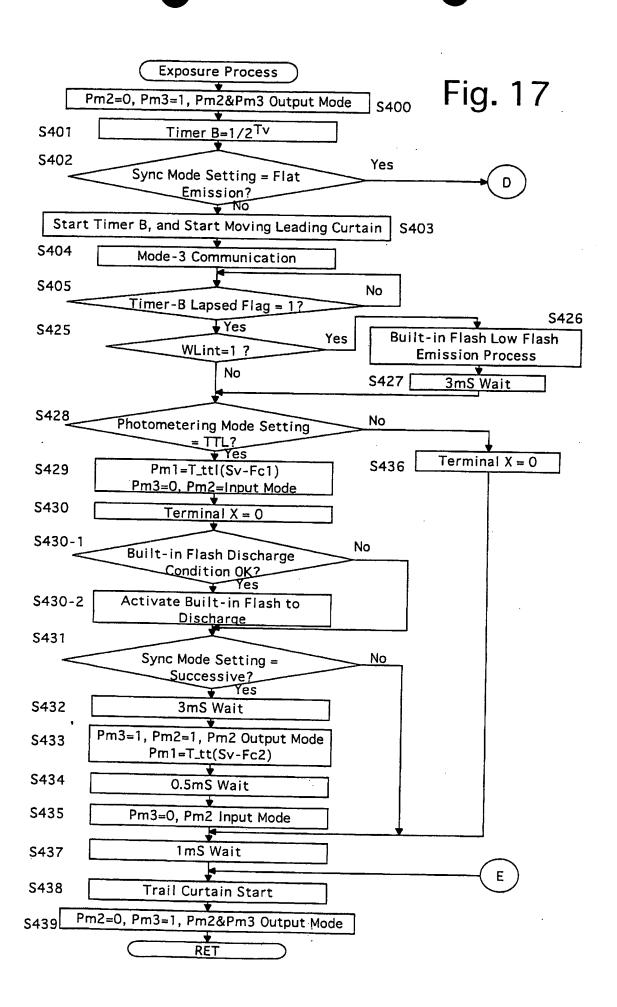


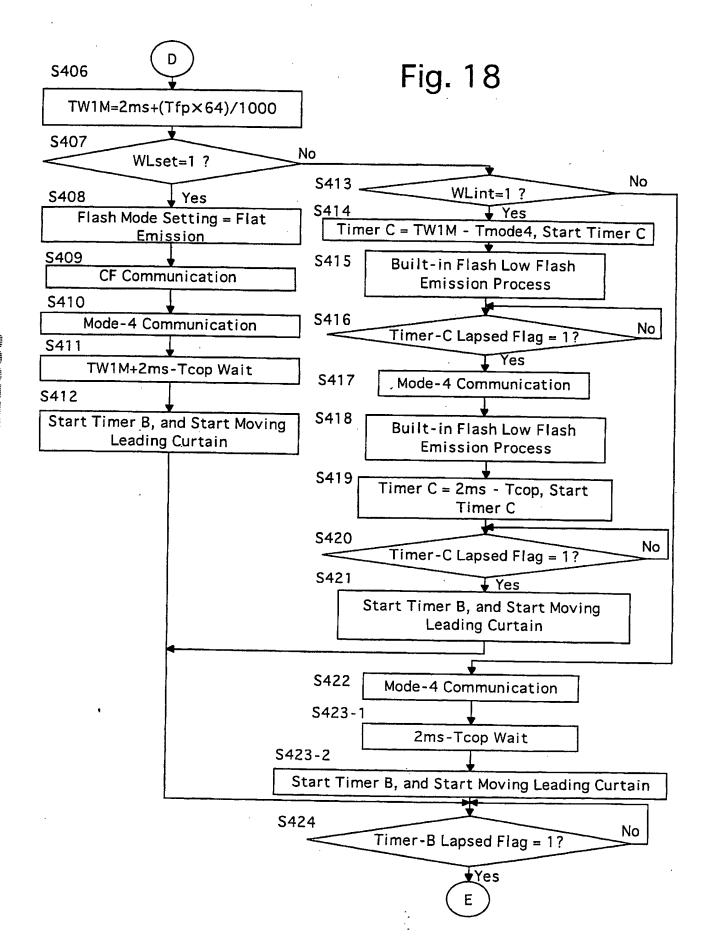


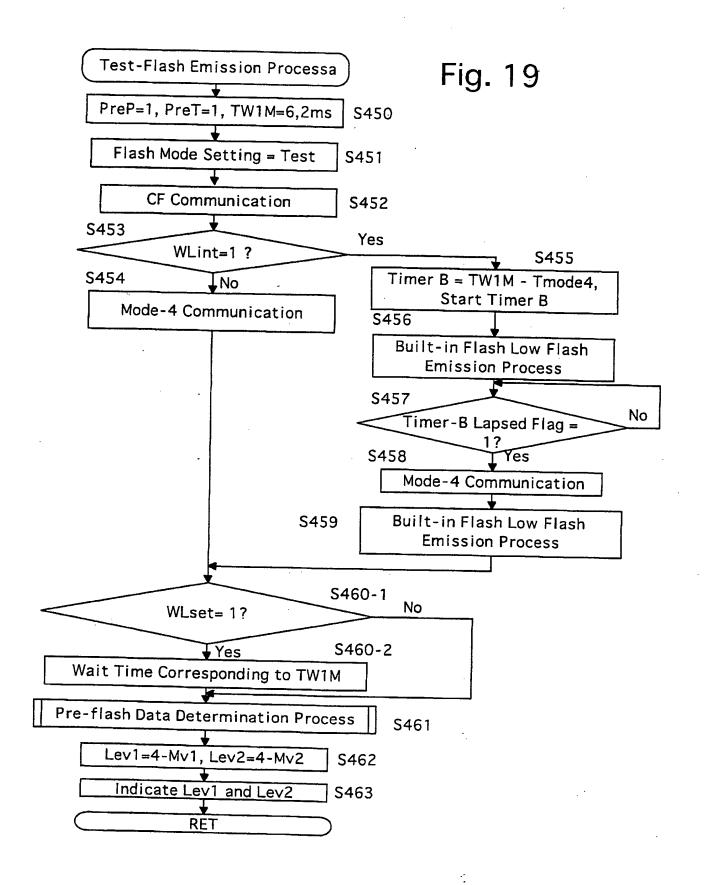


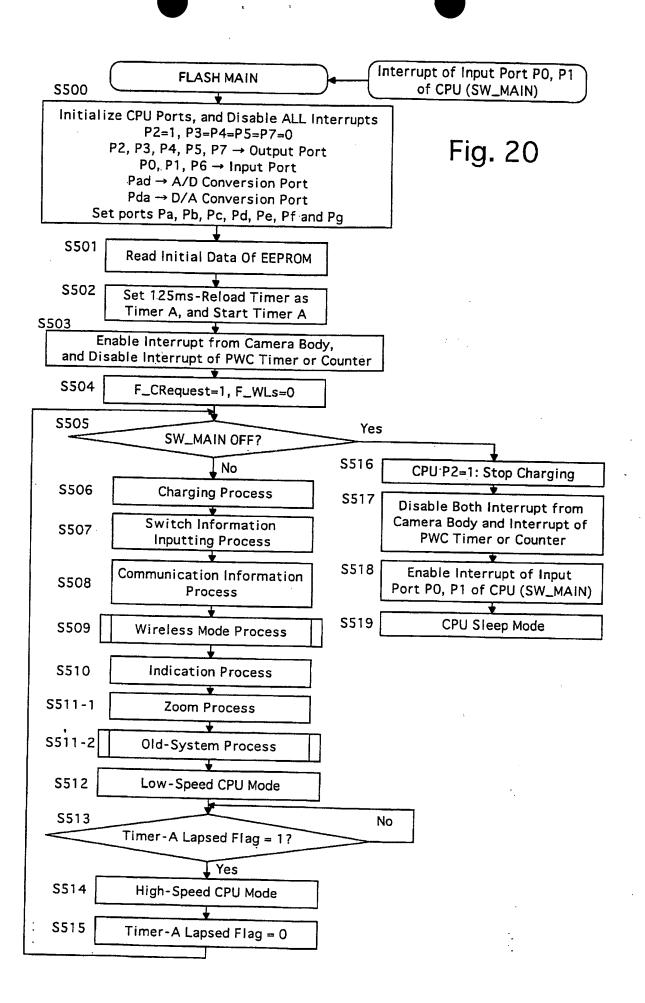


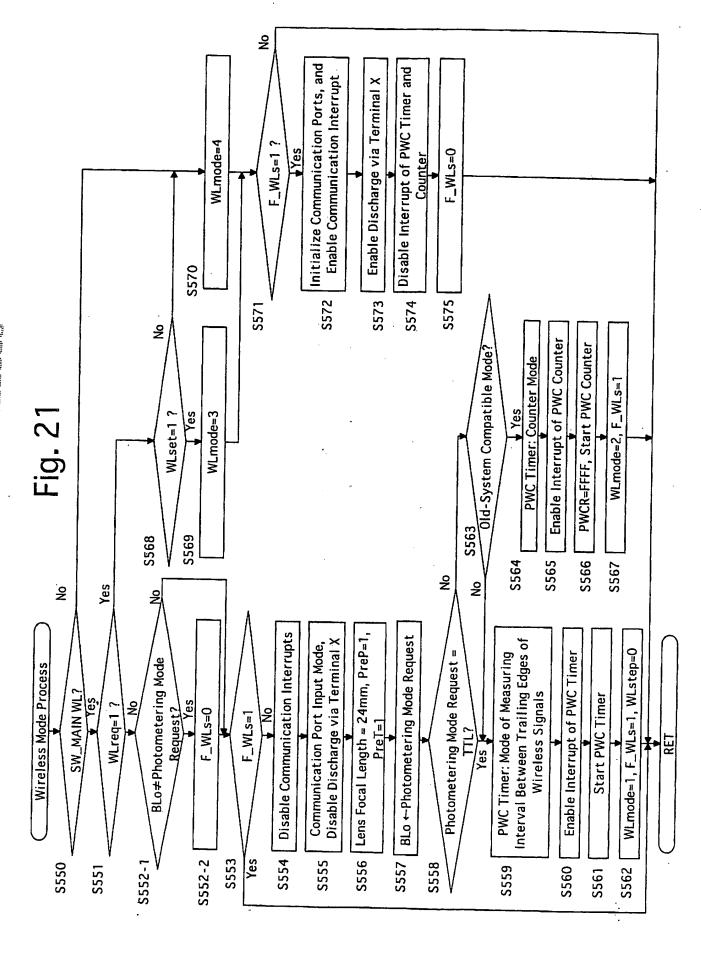


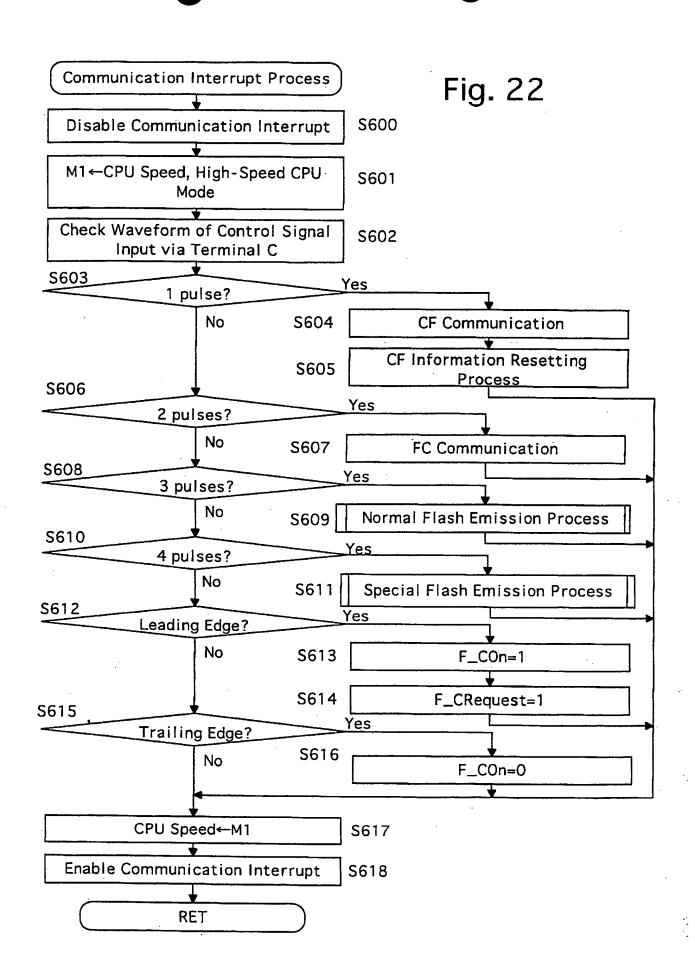


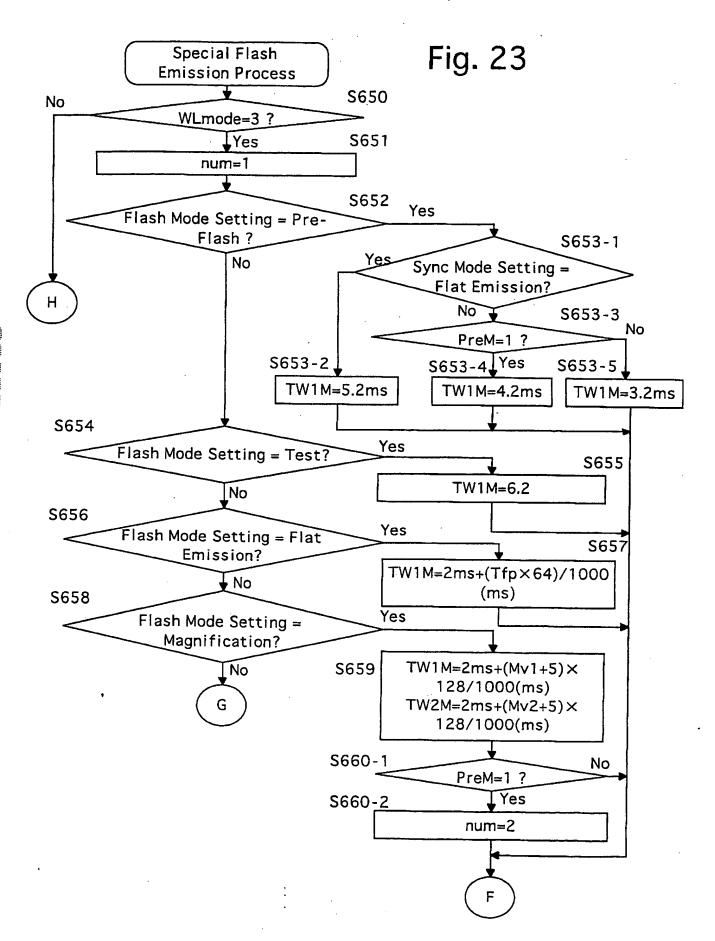


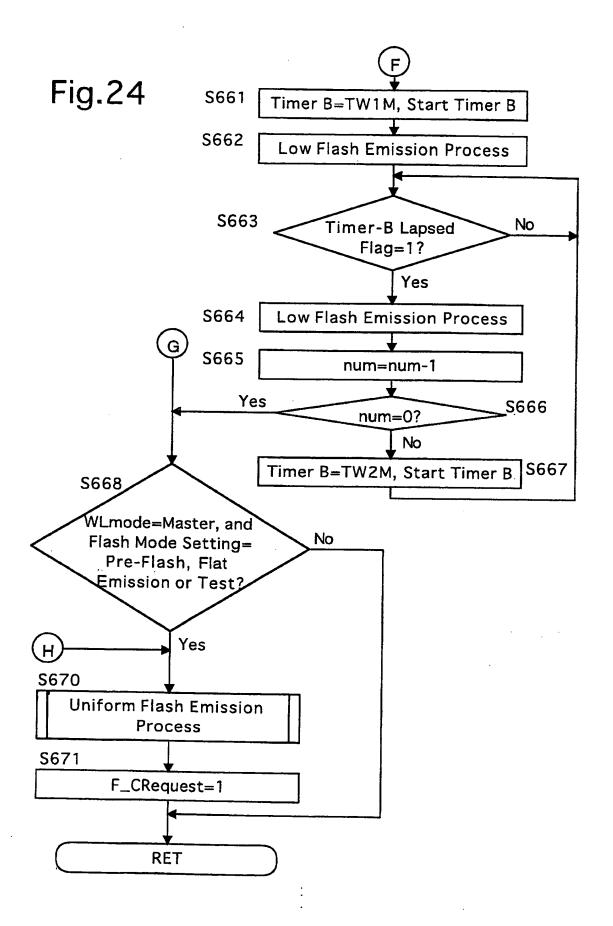


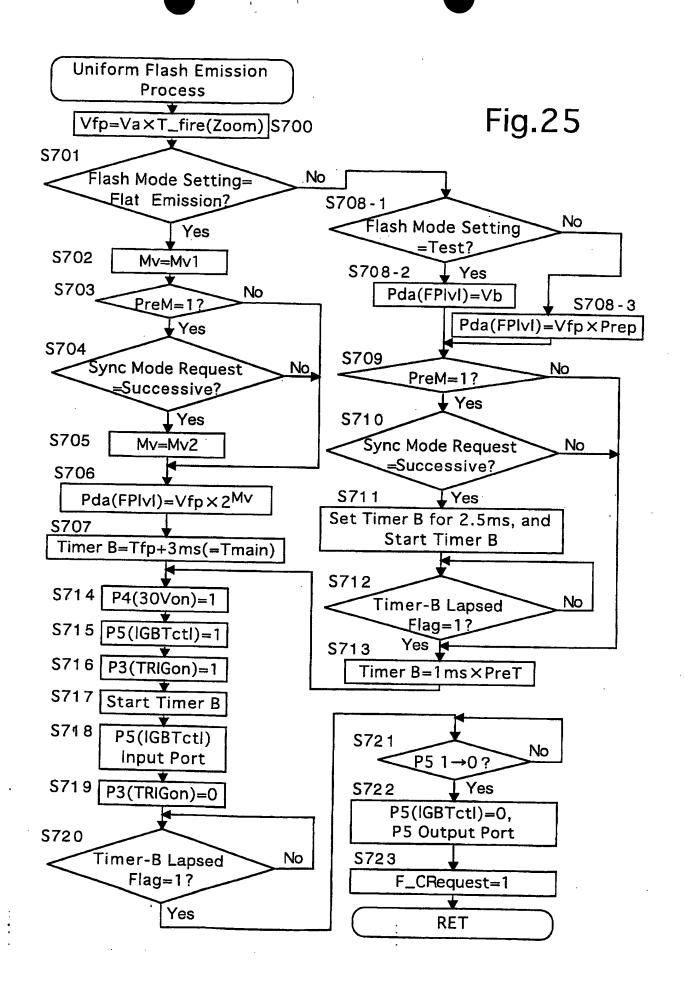


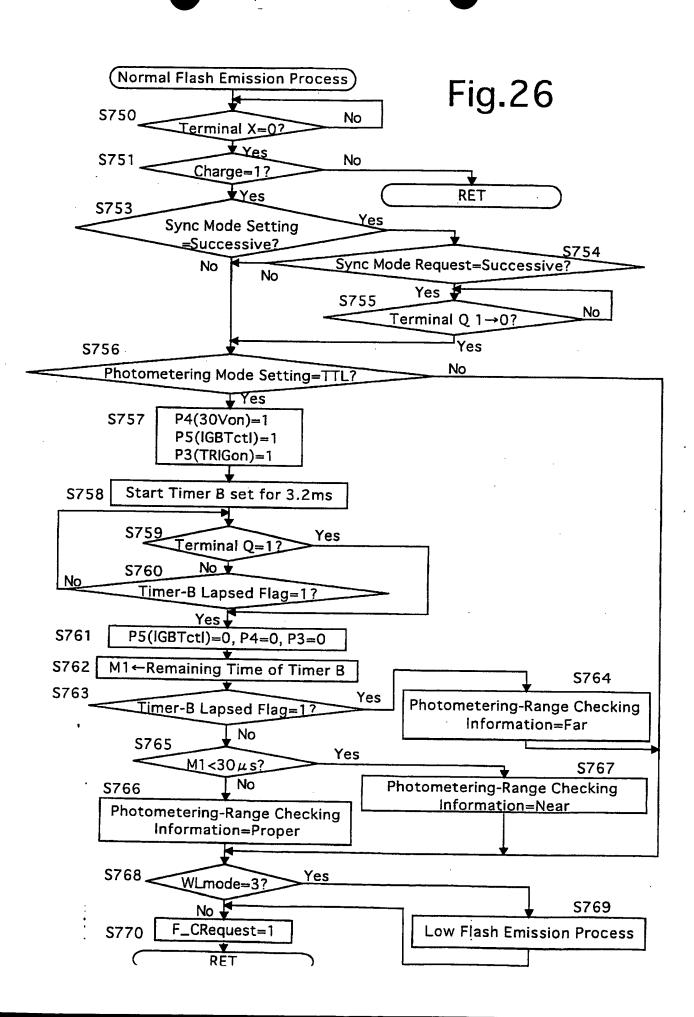


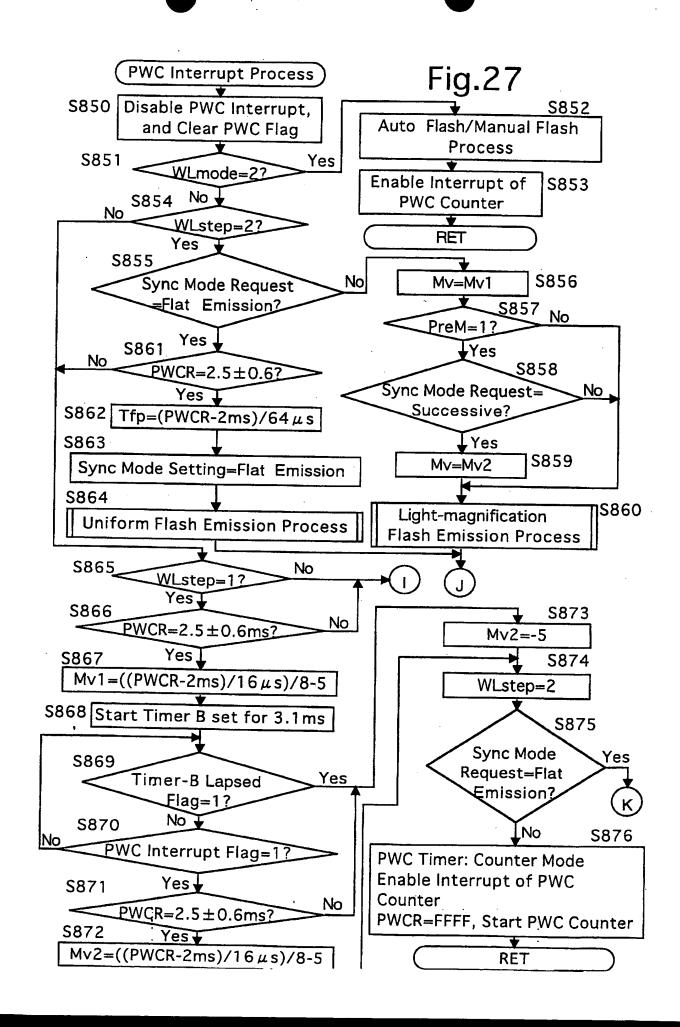


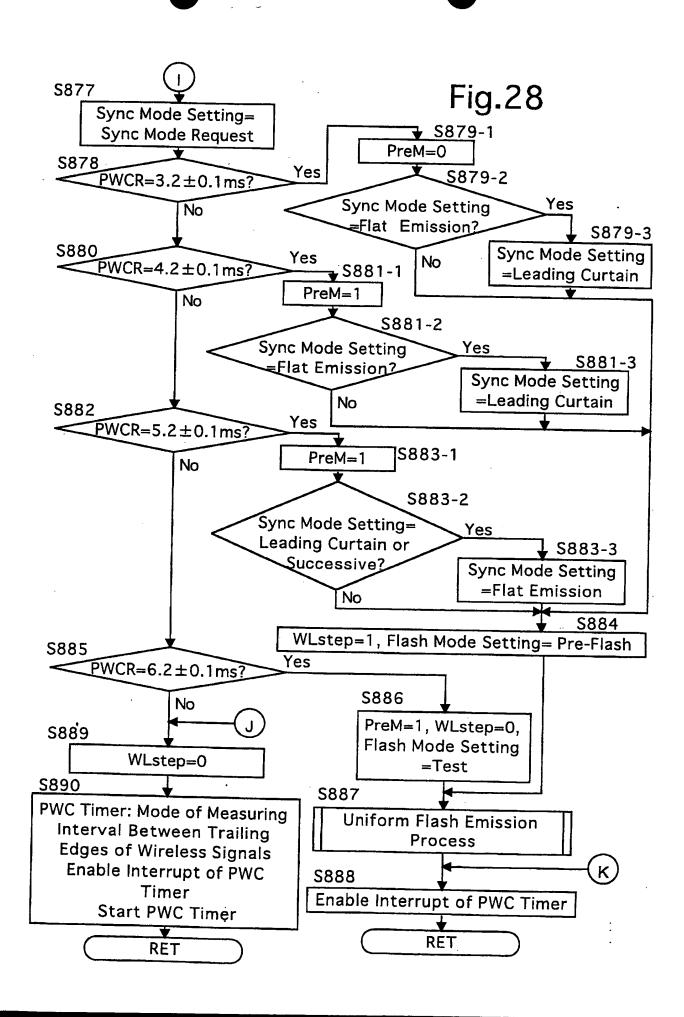












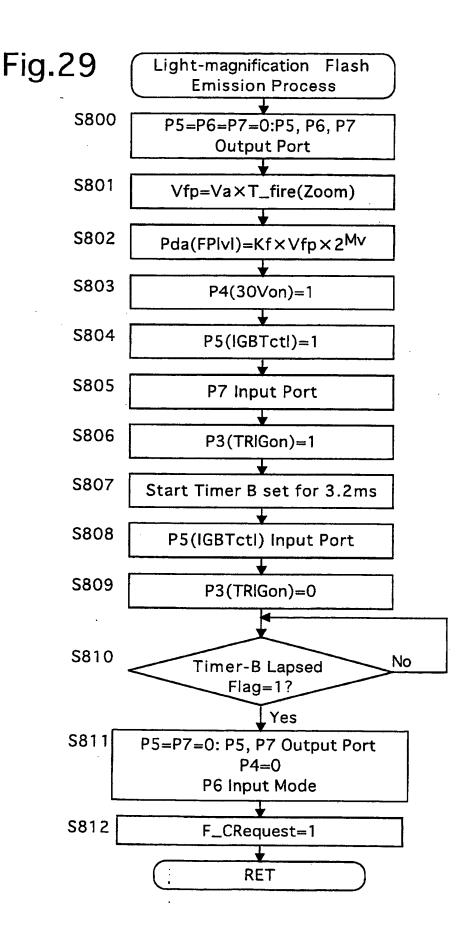


Fig.30

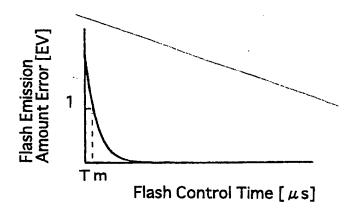


Fig.31

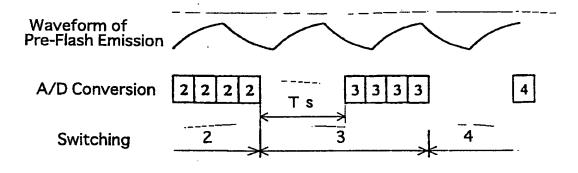


Fig.35

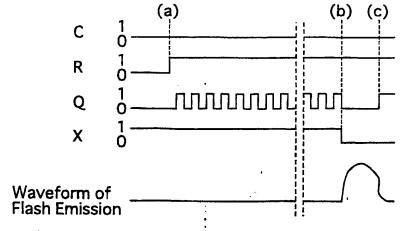


Fig.32

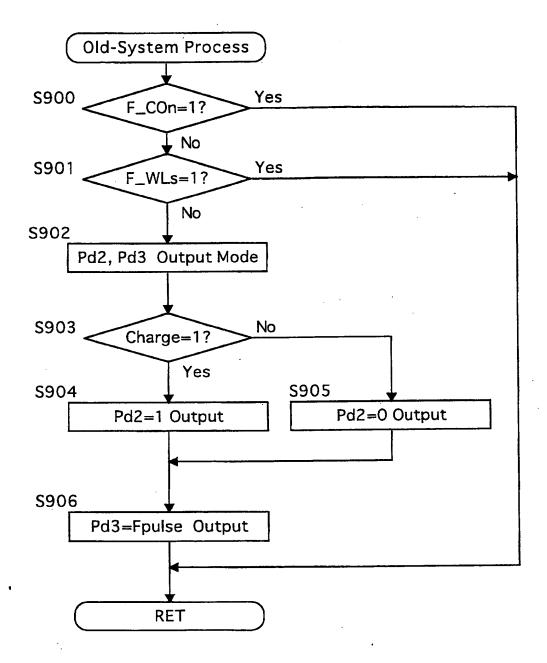


Fig.33

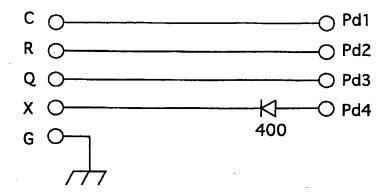


Fig.34

